

**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: )  
 )  
Chen, Addison ) Art Unit: 2133  
 )  
Application No. 10/783,345 ) Examiner: Alphonse, Fritz  
 )  
Filed: 02/20/2004 ) Atty. Docket No.  
 ) NVIDP341/P001326  
For: SYSTEM AND METHOD FOR )  
GENERATING 128-BIT CYCLIC ) Date: April 06, 2007  
REDUNDANCY CHECK VALUES WITH )  
32-BIT GRANULARITY )

*Please  
Enter*

*5/24/2007  
F.A.*

AMENDMENT AFTER NOTICE OF ALLOWANCE AND BEFORE PAYMENT  
OF THE ISSUE FEE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Examiner:

In response to the Notice of Allowance mailed 03/13/2007, please enter the following amendments.